## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant** 

: Salman Akram

Serial No.

: 09/992,580

Filed

: November 16, 2001

Title

: DIE STACKING SCHEME

Docket

MIO 0072 VA

Art Unit

: 2815

Confirm. No.: 9954

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

February 11, 2002

**Assistant Commissioner for Patents** 

Washington, DC 20231

Sir:

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §§ 1.56, 1.97, AND 1.98

Applicant submits herewith patents, publications, and other information of which he is aware, which he believes may be material, as defined in 37 CFR §1.56(b), to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56(a). While the information referred to in this Information Disclosure Statement may be material pursuant to 37 CFR §1.56(b), the filing of this Information Disclosure Statement is not intended to, pursuant to 37 CFR §1.97(h), constitute an admission that any patent, publication, or other information referred to is, or is considered to be, material to the patentability of this invention. Further, pursuant to 37 CFR §1.97(g), the filing of this Statement should not be construed as a statement that a search has been made or that no other material information exists.

The above application is a division of U.S. Application Serial No. 09/804,421 filed March 12, 2002. Pursuant to §1.98(d), copies of those items cited by or submitted to the Office in the prior application are not being submitted, but are listed on PTO/SB/08A, a substitute for form 1449A/PTO.

Serial No. 09/992,580 Docket MIO 0072 VA

This Information Disclosure Statement is being filed within the period set forth in 37 CFR §1.97(b) because it is filed within three months of the filing date of a national application or within three months of the date of entry of the national stage as set forth in 37 CFR §1.491 in an international application.

Respectfully submitted, KILLWORTH, GOTTMAN, HAGAN & SCHAEFF, L.L.P.

By

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JEB/amm Encl.

PTO/SB/08A (10-01)

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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

of Sheet

Complete if Known		
Application Number	09/992,580	
Filing Date	November 16, 2001	
First Named Inventor	Salman Akram	
Art Unit	2815	
Examiner Name		
Attorney Docket Number	MIO 0072 VA	

		U.S. PAT	ENT DOCUMENTS	
Examiner Initials	Cite No. 1 Number - Kind Code 2 (if know	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Initials	Cite No. 1	Foreign Patent Document  Country Code 3 - Number 4 - Kind Code 5 (# known)	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		WO 97/37374 A2	10/09/1997	Herrell, Dennis J.	<u> </u>	L
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PTO/SB/08B (10-01)

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Complete if Known Substitute for form 1449B/PTO 09/992,580 **Application Number** INFORMATION DISCLOSURE November 16, 2001 Filing Date First Named Inventor Salman Akram STATEMENT BY APPLICANT Group Art Unit 2815 (use as many sheets as necessary) **Examiner Name** 2 of Attorney Docket Number MIO 0072 VA Sheet

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s) publisher, city and/or country where published.	T <sup>2</sup>
		"Multi-Chip Module on Laminate, High-performance packaging for today's silicon", IBM Microelectronics, 1998	
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hari-mayan ga ga shikin daka gilakan alakin ƙa <b>sara</b>			
	OWN I Williams		
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